

GA05JT12-247

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1200 V

280 mΩ

1.4 V

5 A

 V_{DS}

ID

V_{DS(ON)}

R_{DS(ON)}

Normally – OFF Silicon Carbide Super Junction Transistor

Features

- 175 °C maximum operating temperature
- Temperature independent switching performance
- Gate oxide free SiC switch
- Suitable for connecting an anti-parallel diode
- · Positive temperature coefficient for easy paralleling
- Low gate charge
- · Low intrinsic capacitance

Package

RoHS Compliant



TO-247AB

Advantages

- Low switching losses
- Higher efficiency
- High temperature operation
- High short circuit withstand capability

Applications

- Down Hole Oil Drilling, Geothermal Instrumentation
- Hybrid Electric Vehicles (HEV)
- Solar Inverters
- Switched-Mode Power Supply (SMPS)
- Power Factor Correction (PFC)
- Induction Heating
- Uninterruptible Power Supply (UPS)
- Motor Drives

Maximum Ratings unless otherwise specified

0				
Parameter	Symbol	Conditions	Values	Unit
Drain – Source Voltage	V _{DS}	$V_{GS} = 0 V$	1200	V
Continuous Drain Current	I _D	T _{C,MAX} = 95 °C	5	А
Gate Peak Current	I _{GM}		5	А
Reverse Gate – Source Voltage	V _{SG}		25	V
Reverse Drain – Source Voltage	V _{SD}		25	V
Power Dissipation	P _{tot}	T _c = 25 °C	5	W
Storage Temperature	T _{stg}		-55 to 175	°C

Electrical Characteristics at T_i = 175 °C, unless otherwise specified

Deveryor	Quarte el	Conditions	Values		11	
Parameter	Symbol	Conditions -	min.	typ.	max.	Unit
On Characteristics						
		I _D = 5 A, I _G = 100 mA, T _j = 25 °C		1.4		
Drain – Source On Voltage	V _{DS(ON)}	I _D = 5 A, I _G = 200 mA, T _j = 125 °C		1.6		V
		I _D = 5 A, I _G = 400 mA, T _j = 175 °C		2.2		
		I _D = 5 A, I _G = 100 mA, T _j = 25 °C		280		
Drain – Source On Resistance	R _{DS(ON)}	I _D = 5 A, I _G = 200 mA, T _i = 125 °C		320		mΩ
		$I_{D} = 5 \text{ A}, I_{G} = 400 \text{ mA}, T_{i} = 175 \text{ °C}$		440		
	$V_{GS(FWD)}$	I _G = 500 mA, T _i = 25 °C		3.3		
Gate Forward Voltage		I _G = 500 mA, T _i = 175 °C		3.1		V
DO Ourrent Onlin	0	$V_{DS} = 5 V, I_{D} = 5 A, T_{i} = 25 °C$		TBD		
DC Current Gain	β	V _{DS} = 5 V, I _D = 5 A, T _j = 175 °C		TBD		
Off Characteristics						
		V _R = 1200 V, V _{GS} = 0 V, T _i = 25 °C		210		
Drain Leakage Current	IDSS	V _R = 1200 V, V _{GS} = 0 V, T _j = 125 °C		310		nA
		V _R = 1200 V, V _{GS} = 0 V, T _i = 175 °C		420		



GA05JT12-247

Parameter	Symbol	Symbol Conditions	Values				
Parameter	Symbol	Conditions	min.	typ.	max.	Unit	
Switching Characteristics							
Turn On Delay Time	t _{d(on)}			tbd		ns	
Rise Time	t _r	$V_{DD} = 800 \text{ V}, I_D = 5 \text{ A},$		tbd		ns	
Turn Off Delay Time	t _{d(off)}	$R_{G(on)} = R_{G(off)} = 44 \Omega,$ V _{GS} = -8/15 V, L = 1.1 mH,		tbd		ns	
Fall Time	t _f	FWD = GB10SLT12,		tbd		ns	
Turn-On Energy Per Pulse	Eon	T _j = 25 °C		tbd		μJ	
Turn-Off Energy Per Pulse	E _{off}	Refer to Figure 11 for gate current		tbd		μJ	
Total Switching Energy	E _{ts}	waveform		tbd		μJ	
Turn On Delay Time	t _{d(on)}			tbd			
Rise Time	t _r	$V_{DD} = 800 \text{ V}, I_D = 5 \text{ A},$		tbd		ns	
Turn Off Delay Time	t _{d(off)}	$\begin{array}{c} R_{G(on)} = R_{G(off)} = 44 \ \Omega, \\ V_{GS} = -8/15 \ V, \ L = 1.1 \ mH, \\ FWD = GB10SLT12, \\ T_{j} = 175 \ ^{\circ}C \\ Refer to Figure 11 \ for gate current \\ waveform \end{array}$		tbd		ns	
Fall Time	t _f			tbd		ns	
Turn-On Energy Per Pulse	Eon			tbd		μJ	
Turn-Off Energy Per Pulse	E _{off}			tbd		μJ	
Total Switching Energy	E _{ts}			tbd		μJ	

Electrical Characteristics at T_j = 175 °C, unless otherwise specified

Thermal resistance, junction - case	R _{thJC}	1.64	°C/W





Figure 1: Typical Output Characteristics at 25 °C

Figure 2: Typical Output Characteristics at 125 °C







Figure 3: Typical Output Characteristics at 175 °C

Figure 4: Typical Gate Source I-V Characteristics vs. Temperature





Figure 5: Normalized On-Resistance and Current Gain vs. Temperature

Figure 6: Typical Blocking Characteristics

TBD



Figure 7: Typical Hard-switched Turn On Waveforms

Figure 8: Typical Hard-switched Turn Off Waveforms







Figure 9: Typical Turn On Energy Losses and Switching Times vs. Temperature Figure 10: Typical Turn Off Energy Losses and Switching Times vs. Temperature



Figure 11: Typical Gate Current Waveform



Gate Drive Technique (Option #1)

To drive the GA05JT12-247 with the lowest gate drive losses, a custom-designed, dual voltage source gate drive configuration is recommended [for example, see Figure 5(a) in J. Rabkowski et al. IEEE Trans. Power Electronics 27(5), 2633-2642 (2012)]. More details on using this optimized gate drive technique will be made available shortly. An effective simple alternative for ultra-fast switching of the GA05JT12-247 is available below.

Gate Drive Technique (Option #2)

The GA05JT12-247 can be effectively driven using the IXYS IXDN614 / IXDD614 non-inverting gate driver IC or a comparable product. A typical gate driver configuration along with component values using this driver is offered below. Additional information is available from the manufacturer at www.ixys.com.

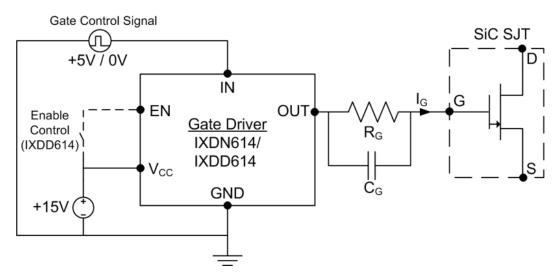


Figure 14: Recommended Gate Diver Configuration (Option #2)

Devenuestan	Symbol	Symbol Conditions	Values			1114
Parameter	Symbol		min.	typ.	max.	Unit
Gate Driver Pins (IXDD614/IXDN614)						
Supply Voltage	V _{CC}		-0.3	15	40	V
Gate Control Input Signal, Low	IN		-5.0	0	0.8	V
Gate Control Input Signal, High	IN		3.0	5.0	V _{cc} +0.3	V
Enable, Low	EN	IXDD614 Only			1/3*V _{CC}	V
Enable, High	EN	IXDD614 Only	2/3*V _{cc}			V
Output Voltage, Low	V _{OUT}				0.025	V
Output Voltage, High	Vout		V _{CC} -0.025			V
Output Current, Peak	I _{OUT}	Package Limited		4.5	14	А
Output Current, Continuous	I _{OUT}			0.5	4.0	А
Passive Gate Components			•		•	
Gate Resistance	R _G	l _G ≈0.5 A	5	22		Ω

I_G ≈ 0.5 A

Gate Capacitance

 C_{G}

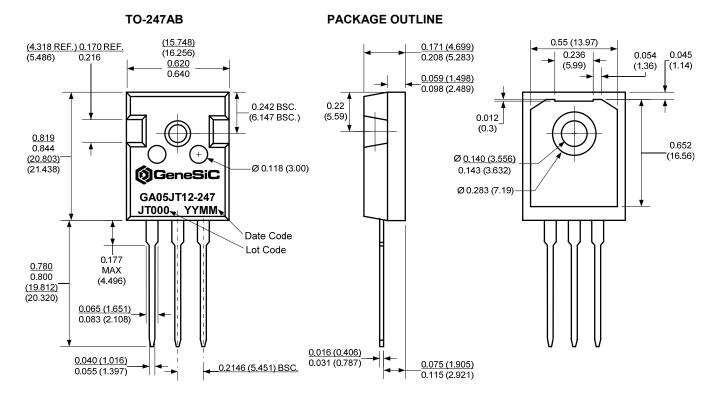
nF

100





Package Dimensions:



NOTE

1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.

2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS

Revision History					
Date	Revision	Comments	Supersedes		
2013/01/15	0	Initial release			

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